

Low-Cost Wake-Up Receiver for RF Energy Harvesting Wireless Sensor Networks

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Abstract—The existing passive wake-up receivers (WuRx) are radio frequency identification (RFID) tag based, which incur high cost and complexity. In this paper, we study cost-effective and long-range WuRx solutions for range-based wake-up (RW) as well as directed wake-up (DW). In particular, we consider the case of an RF energy harvesting wireless sensor node and investigate how a low-cost WuRx can be built using an RF energy harvester available at the node. Experimental results show that our developed prototype can achieve a wake-up range of 1.16 m with +13 dBm transmit power. Furthermore, our empirical study shows that at +30 dBm transmit power the wake-up distance of our developed RW module is >9 m. High accuracy of DW is demonstrated by sending a 5-bit ID from a transmitter at a bit rate up to 33.33 kbps. Finally, we present optimized WuRx designs for RW and DW using Agilent advanced design system, which offer up to 5.69 times higher wake-up range for RW and energy savings per bit of about 0.41 mJ and 21.40 nJ, respectively, at the transmitter and the sensor node in DW.

Index Terms—RF energy harvesting, passive wake-up receiver, range-based wake-up, directed wake-up, wireless sensor network.

I. INTRODUCTION

LIFETIME of a wireless sensor node is constrained by its battery energy. Recharging through dedicated RF energy transfer [2], [3] using an RF energy harvester (RFEH) can enable its perpetual operation. However, in order to avoid frequent recharging, the battery energy should be judiciously used. Software solutions such as duty-cycled MAC protocols [4] use the energy wisely by keeping the node mostly in deep-sleep state. Yet, the nodes suffer from idle listening and overhearing, leading to increased energy consumption. These problems can be alleviated by using a wake-up receiver (WuRx) based solution [5]. A WuRx-enabled node

can switch off its main radio and stay in deep-sleep state unless it needs to sample the sensor or is woken up by another node using a wake-up signal. Thus, a WuRx complements an RFEH in extending the node lifetime. *Thanks to the presence of an RFEH on the node in an RF energy harvesting wireless sensor network (WSN), a low-cost WuRx can be built with a few changes to the existing resources of the node.*

A. Prior Art

RF Energy Harvesters: An RFEH converts the RF power available at its antenna to DC power [6]. An ideal RFEH is capable of harvesting RF energy simultaneously from multiple RF sources operating in different frequency bands with 100% RF-to-DC conversion efficiency [7]. However, various factors, such as, non-zero turn-on voltage of the diodes used for rectification, losses due to improper matching, and leakage current of the rectification diodes, are the bottlenecks [8].

In terms of reception sensitivity, commercially available RFEHs, such as Powercast P1110 [9], require a minimum input RF power of about -11 dBm. This requirement is quite higher than in most state-of-the-art prototypes [6], [8]. Further advances have led to the development of RFEHs that can harvest simultaneously from multiple RF-bands [7] and use adaptive control techniques to achieve high conversion efficiency at low input RF powers [6], [10].

Wake-Up Receivers: Depending on the energy source, a WuRx can be classified as: *active* – that draws energy from the node’s battery for receiving a wake-up signal, and *passive* – that derives energy from the wake-up signal. The wake-up technique can be categorized into *range-based wake-up (RW)* – where all the nodes wake up upon receiving a wake-up signal, and *directed wake-up (DW)* – where a specific node is woken up by ID match [5]. *An ideal WuRx should be passive, have high reception sensitivity and large wake-up range, and be capable of both RW and DW.* In [11], a radio triggered circuit based passive WuRx was introduced for RW, where it was shown through SPICE simulations that the wake-up range can be up to 30 meters with a 55 ms delay. DW function using multiple transceivers was also presented. However, hardware implementation studies were not conducted in that work.

Another passive wake-up radio, called WISP-mote, was proposed in [12] using Intel wireless identification and sensing platform (WISP). The wake-up range was shown to be 4 meters. Computing capabilities of the MSP430F2132 microcontroller (μ C) in WISP were used for ID decoding in DW.

Manuscript received April 20, 2016; accepted May 19, 2016. Date of publication June 1, 2016; date of current version July 18, 2016. This work was supported in part by the Department of Electronics and Information Technology under Grant DeitY 13(2)/2012CC&BT and the Department of Science and Technology (DST) under Grant SB/S3/EECE/0248/2014, and in part by the National Science Foundation through the Green Sensor Networks for Air Quality Support Project under Grant NSF CNS 1143662 and Grant NSF CNS 1452628. This paper was presented at the IEEE Sensors Conference, Busan, South Korea, November 2015. The associate editor coordinating the review of this paper and approving it for publication was Prof. Aime Lay-Ekuakille.

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Digital Object Identifier 10.1109/JSEN.2016.2574798

Later, it was experimentally demonstrated in [13] that WISP-mote wake-up range can be increased by using an additional RFEH. The authors in [13] also presented a passive design, called REACH-mote, that uses a reset circuit [14] along with an RFEH [8]. It was shown that, REACH-mote can provide a wake-up range of about 7.92 meters with a Powercast transmitter having an effective isotropic transmit power (EIRP) of 3 W [9]. However, REACH-mote cannot perform DW.

Recently, a long-range passive WuRx design for RW was proposed in [15] by using an additional DC-DC converter. The authors also performed DW of an IRIS mote connected to a Monza X-2K passive RFID tag IC using its write wake-up. Though this design can achieve a large wake-up range, the DC-DC converter incurs significant hardware cost.

B. Motivation

As discussed above, most of the existing methods for node wake-up involves an overhead, either in terms of extra energy consumption or additional hardware. For example, a single pulse providing an input voltage greater than a threshold voltage V_{\min} at the interrupt pin is sufficient for RW instead of using multiple pulses generated by a reset circuit [14] as shown in [13]. However, we note that an RFEH providing an output DC power at a certain voltage $V_{\text{out}}^{\text{DC}}$, can act as a potential interrupt source when $V_{\text{out}}^{\text{DC}} \geq V_{\min}$. Therefore by directly connecting the output of an RFEH to the interrupt pin of the μC , one could not only obtain the same functionality provided by the reset circuit, but also reduce the current consumed by the reset circuit and obtain a sensor node with a small form factor. In this way, a single RFEH can be used for both operations, namely, energy harvesting and RW.

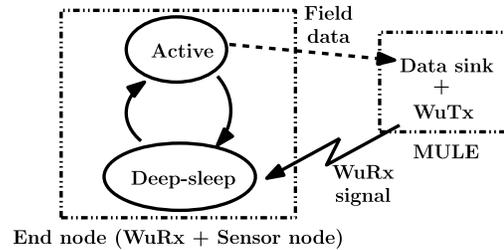
To the best of our knowledge, there is no existing device that performs both RW and DW using the same hardware and provides as long a wake-up range in DW as in RW. With the help of RFEH-based WuRx, we are able to demonstrate a different paradigm of *hybrid wake-up* where DW occurs by performing multiple RW in a given time window using the same RFEH and ultra-low power μC of the sensor node.

C. Scope and Contributions

In this work, we first develop a hardware prototype of the WuRx and experimentally evaluate its performance. In order to have a compact design, we use the same antenna for RF energy harvesting as well as wake-up. Besides implementing a passive WuRx for RW, we develop an active WuRx design for DW. Since the WuRx is developed using an in-node RFEH, the achievable wake-up range and delay are limited by the underlying RFEH characteristics [16]. As an extension to our RFEH-based WuRx design, we also present two other solutions, which are independent of the node's RFEH.

In contrast to [13], our proposed WuRx does not require any reset circuit for its operation. Beyond the solution in [15], our module can perform both RW and DW with no changes to the hardware. Highlights and key contributions of this work are:

- i) A low-cost RFEH-based WuRx is proposed that supports both wake-up and energy harvesting functions.



End node (WuRx + Sensor node)

Fig. 1. Field sensor node wake-up (RW or DW) state diagram.

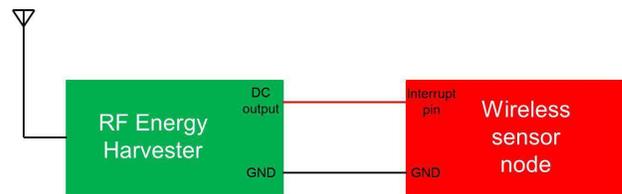


Fig. 2. Proposed simplified wake-up circuit.

- ii) Implementation of RW provides insights on wake-up range at different values of transmit power.
- iii) A protocol for decoding the ID in DW is developed. The accuracy of DW to selectively waking up a node using a 5-bit code ID is evaluated from hardware experiments for different values of bit duration.
- iv) An empirical model is developed for characterizing the wake-up range versus transmit power for RW and DW.
- v) Optimized WuRx designs independent of the underlying RFEH are proposed for efficient RW and DW.

II. PROPOSED RFEH-BASED WURX DESIGN

A delay-tolerant WSN comprised of a mobile ubiquitous LAN extension (MULE) for collecting data from static field sensor nodes is considered [3]. In order to save energy in the field sensor nodes, they are programmed to remain normally in deep-sleep state. For field data collection, the MULE sends a wake-up signal (RW or DW) to activate the node. After the data transfer, the node goes back to the deep-sleep state until it receives another wake-up signal. Fig. 1 shows the radio communications and the sensor node's state transition.

The block diagram of our proposed RFEH-based WuRx is shown in Fig. 2. Unlike in the REACH-mote [13], our proposed WuRx module does not require a reset circuit. As will be shown in Section IV, the same prototype can also perform directed wake-up, which was not possible with the REACH-mote approach. We next discuss the choice of RFEH that we have considered throughout this paper.

A. Attributes of RFEH

Two main features of WuRx are its wake-up range and wake-up delay. In our case, as the RFEH also acts as a WuRx, the attributes, namely, *sensitivity and responsiveness* of the WuRx can be mapped to those of the RFEH. *In particular, an RFEH with high sensitivity and fast responsiveness acts as a WuRx with long wake-up range and small wake-up delay, respectively.* Although any RFEH operating with low RF energy can be used, in our initial experimental and

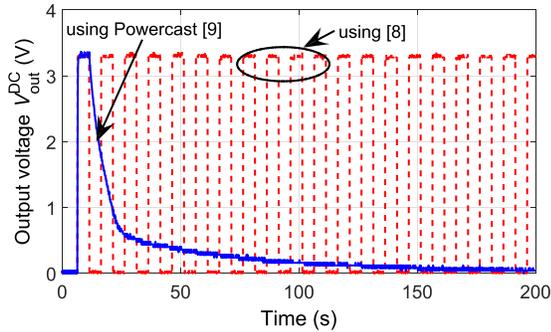


Fig. 3. Responsiveness to RF signal input in [8] and [9].

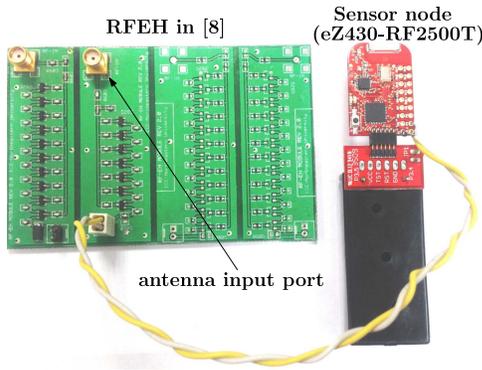


Fig. 4. Prototype of RFEH-based WuRx enabled sensor node.

empirical evaluations we chose the 7-stage RFEH design presented in [8], which was originally designed as an efficient alternative to Powercast RFEH [9]. The RFEH in [8] is based on a Dickson voltage multiplier circuit, built using HSMS-2852 Schottky diodes that have low turn-on voltage of 0.15V. As the RFEH is tuned to operate at a frequency of 915 MHz, our proposed WuRx also operates at this frequency.

B. Comparison of RFEH in [8] With Powercast P1110 [9]

We have conducted experiments to find the responsiveness of the RFEH in [8] and that of the Powercast P1110. To this end, an RFEH was allowed to harvest energy transmitted by an RF source in alternate 5 second periods using ON-OFF keying (OOK) modulation. Fig. 3 shows that the design in [8] is highly responsive to the ON and OFF transitions (rise/fall times of V_{out}^{DC} on the order of a few tens of μ s). Hence it can be used for DW, where the RF source (WuTx) sends OOK modulated ID of the sensor node as the wake-up signal. In contrast, although Powercast P1110 RFEH has small rise time, its fall time is on the order of a few tens of seconds, which is due to a large capacitor in the output stage of Powercast P1110 RFEH.

C. Prototype of RFEH-Based WuRx

Fig. 4 shows the prototype of our proposed RFEH-based WuRx enabled sensor node. The sensor node is comprised of a Texas Instruments (TI) eZ430-RF2500T having a 16-bit ultra-low power μ C TI MSP430F2274 and a 2.4 GHz

transceiver CC2500. The SMA connector on the RFEH is used to connect an antenna that operates at 915 MHz. Hereafter, the sensor node connected to the RFEH is termed as *end node*. Although the prototype has an RFEH, this harvester could support the operation of a node only in sleep state when it draws a few μ A, but not in the active state when the node consumes about 270 μ A/MHz at 2.2 V. Due to this, the sensor node derives its energy for its regular sensing and common operations from an on-board battery, while the RFEH is used for passive wake-up. Next we present the RW experiments and results.

III. RANGE-BASED WAKE-UP

Within the safe operation limits of the μ C in a sensor node, V_{out}^{DC} of an RFEH can be categorized as LOW or HIGH. Thus, the input RF power above a minimum threshold $P_{r,min}$ causes its V_{out}^{DC} to go from LOW to HIGH. In other words, this transition of DC output voltage generates a positive edge trigger, which interrupts the sleeping mote's μ C to transit to the active state. Note that a Zener diode is placed at the output of the RFEH to protect the sensor node's μ C from potentially high voltage of the RFEH.

A. Implementation

By default, the node is kept in *deep-sleep* state by switching off the radio and programming the MSP430F2274 μ C to stay in low power mode 3 (LPM3). The wake-up signal triggers the μ C to transit from LPM3 to *active* state. After that, the mote's radio is switched on and a wake-up response (which may include field data) is sent to the WuTx. After the communication, the end node returns to *deep-sleep* state. For our experimental study, an Agilent N9310A RF source connected to a Powercast 6.1 dBi antenna was used to send a wake-up signal at 915 MHz. Another 6.1 dBi Powercast antenna connected to the RFEH (cf. Fig. 4) was used as the prototype WuRx. To find the maximum wake-up range, both the WuTx and WuRx were placed on a line of sight at a height of 80 cm above the floor.

B. Experimental Results for RW

Experiments were limited to short-range communication because the maximum transmit signal power of the RF transmitter used in our experiments is +13 dBm. Fig. 5 shows that the wake-up range of the prototype WuRx at +13 dBm transmit power is 1.16 m. Fig. 5 also shows that the wake-up range of the prototype is identical to that of the REACH-mote design [13]. *It may be recalled that, with respect to the REACH-mote design, the proposed prototype is more cost-effective as it eliminates the need for a reset circuit.*

IV. DIRECTED WAKE-UP

We now present how DW is performed using our developed prototype. Through the experiments we also show how the decoding accuracy depends on bit duration of the ID sequence.

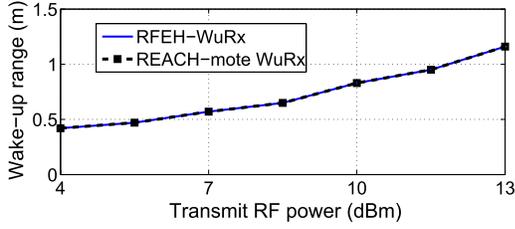


Fig. 5. Effect of transmit power on WuRx range.

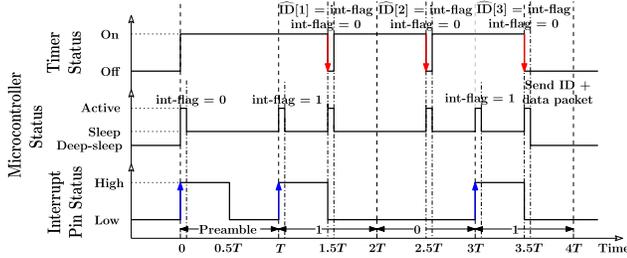


Fig. 6. Directed wake-up timing diagram for ID = 101.

A. Implementation

The main idea behind DW using our proposed RFEH-based WuRx prototype is: *an incoming RF signal generates a sudden increase in harvested energy at the output of the RFEH*. That is, V_{out}^{DC} sharply transits from LOW-to-HIGH upon reception of an RF signal. Also, V_{out}^{DC} goes from HIGH-to-LOW shortly after removal of the RF signal input. This implies that, a suitably-modulated RF wake-up signal may be used to wake-up an intended node. After identifying a series of interruptions within a fixed time, the μC can interpret or decode the target node ID. In Fig. 6, we present a timing diagram for the steps involved in decoding the DW ID ‘101’, with duration of each bit as T s. For transmission of the intended ID, we use:

- *OOK modulation* – to generate an interrupt at the μC upon receiving a ‘1’ bit, none upon receiving a ‘0’ bit.
- *RZ encoding* – to enable detection of consecutive ‘1’ bits in an RF WuTx signal that is modulated by a WuRx ID.
- *1-bit preamble* (‘1’) – to ensure that the WuRx is able to decode even a binary code starting with a ‘0’ bit.

A detailed explanation of the RFEH-based DW procedure, involving reception and decoding, is provided in Algorithm 1.

B. Experimental Study

Different component blocks of the static field node and MULE for DW operation are shown in Fig. 7(a). We have performed hardware experiments to investigate decoding accuracy of the proposed RFEH-based DW at varying bit duration. For this, WuTx of the MULE and WuRx of the end node were placed at a height of 80 cm above the floor, separated by 80 cm. The experimental setup is shown in Fig. 7(b), and the component specifications are listed in Table I.

For experimental evaluation of DW, an end node was assigned an $N = 5$ bit node ID ‘11111’. On reception of an $N + 1$ OOK modulated ‘111111’ wake-up signal (which includes a 1-bit (‘1’) preamble) from the MULE and subsequent ID match, the end node sends an ACK to the data sink,

Algorithm 1. N -bit ID Decoding in Directed Wake-Up

Require: Reception of preamble (‘1’)

Ensure: ID match status $\chi = \begin{cases} 1, & \text{ID match} \\ 0, & \text{otherwise} \end{cases}$

- 1: Set $i \leftarrow 0$, int-flag $\leftarrow 0$, $\widehat{ID} \leftarrow$ a N -dimensional zero vector
- 2: μC status: *deep-sleep* (LPM3) \rightarrow *active*
- 3: Timer status: *OFF* \rightarrow *ON*
- 4: Set *fire* time as $1.5T$ s and μC status: *active* \rightarrow *sleep* (LPM1)
- 5: **repeat** (Main Loop)
- 6: **if** Received bit = ‘1’ **then**
- 7: μC status: *sleep* \rightarrow *active*
- 8: int-flag $\leftarrow 1$
- 9: **end if** \triangleright ‘0’ bit does not produce an interrupt
- 10: On timer expiry, μC status: *sleep* \rightarrow *active*
- 11: $i \leftarrow i + 1$
- 12: $\widehat{ID}[i] \leftarrow$ int-flag, int-flag $\leftarrow 0$
- 13: Set *fire* time as T s and μC status: *active* \rightarrow *sleep*
- 14: **until** ($i \leq N$)
- 15: $\chi \leftarrow \begin{cases} 1, & \widehat{ID} = ID \\ 0, & \text{otherwise} \end{cases}$
- 16: Timer status: *ON* \rightarrow *OFF*
- 17: **if** $\chi = 1$ **then**
- 18: node turns on its transmitter to send its ID+data packet
- 19: **end if**
- 20: μC goes to *deep-sleep* state

TABLE I
SYSTEM SPECIFICATIONS

Device	Specification
Data sink	eZ430-RF2500T + eZ430-RF USB
RF source	Agilent N9310A RF signal generator transmitting at 915 MHz
RZ waveform generator	Agilent 33220A arbitrary waveform generator
RFEH	7-stage voltage multiplier [8]
Sensor node	eZ430-RF2500T + 2 AAA batteries

which is stored as a record in a laptop. This process was repeated 10^4 times to evaluate the DW success rate.

C. Experimental Results for DW

Since DW is carried out by performing multiple RW within a fixed time using the same RFEH, the maximum wake-up range for DW is identical to that of RW. Thus, with our available maximum +13 dBm WuTx power and 6.1 dBi antenna gains, the DW range is about 1.16 m (cf. Fig. 5).

To study the effect of transmission rate on successful wake-up, we varied the bit duration from $10 \mu s$ to $40 \mu s$. For each bit duration, the RZ waveform generator sends the 6 bit DW signal 10^4 times, with a guard time between the adjacent signals. This guard time is to ensure that the end node is able to receive the DW signal and send back an ACK packet to the data sink. Decoding accuracy for various values of bit duration is shown in Fig. 8. Since the considered RZ code in the 5-bit sequence ‘11111’ contains the highest

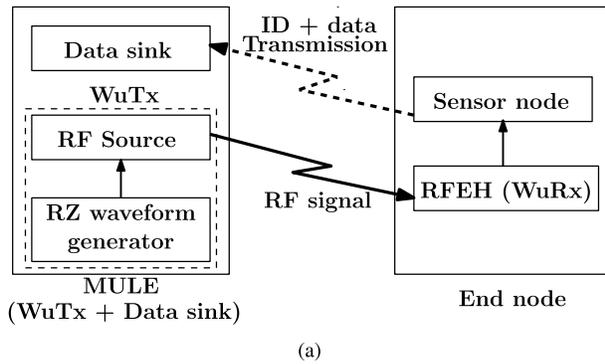


Fig. 7. Directed wake-up. (a) Block diagram of DW and data collection process. (b) Experimental setup for DW.

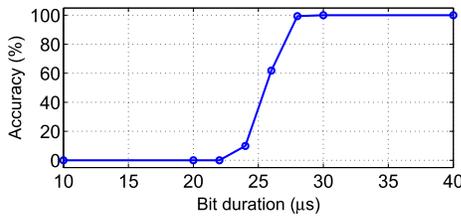


Fig. 8. Decoding accuracy versus bit duration.

number of transitions, this bit stream represents the worst-case scenario of bit errors. Hence, from the plot it can be concluded that a 5-bit DW signal transmitted with a bit rate of up to 33.33 kbps (30 μ s bit duration) can be decoded with 100% accuracy. This bit rate is higher than the achieved rate in some of the recent WuRx techniques. For example, the approach in [17] offers successful wake-up at a bit rate of 0.5 to 8 kbps.

V. EMPIRICAL WAKE-UP RANGE ESTIMATION MODEL

In this section, based on experimental observations of the wake-up range in RW and DW, we develop an empirical model that provides an estimate of the maximum achievable wake-up range with the proposed RFEH-based WuRx design.

A. Variation of V_{min} With Supply Voltage of Mote

The possibility of a RW follows the idea that an interrupt causes the on-board μ C to transit from sleep state to active state. The minimum voltage V_{min} needed for the interrupt to occur depends on the supply voltage V_{CC} of the μ C on the sensor node. As the pins of a μ C contain a Schmitt trigger to avoid input signal noise [18], V_{min} is same as positive going input threshold voltage [19]. We have performed experiments to find the dependence of V_{min} on supply voltage V_{mote} of the mote. Also, the μ C on the TI mote (eZ430-RF2500T) is directly powered by the battery. Therefore, $V_{CC} = V_{mote}$. We have emulated the wake-up process by connecting a voltage supply to the sensor node to power the μ C, and another voltage supply acts as a source for the external interrupts. In order to capture the behavior of motes in general, we have done experiments using a TI mote [20] as well as a Waspote [21].

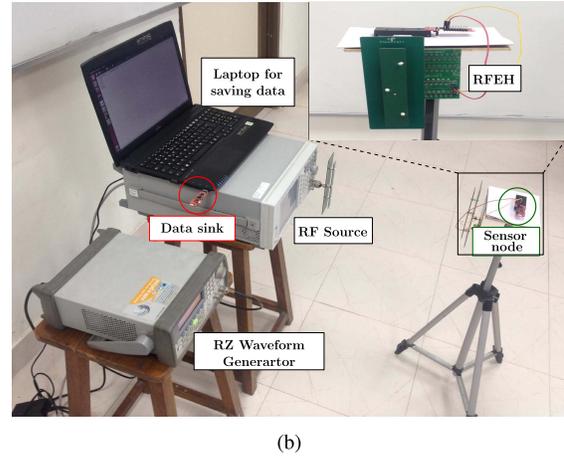


Fig. 9. Minimum voltage for triggering various motes.

Fig. 9 confirms that V_{min} of TI mote at various values of V_{mote} is between the maximum and minimum levels of the positive going input threshold voltage of the TI Schmitt trigger [19]. As the Atmega1281 μ C present on the Waspote uses a 3.3 V low drop-out regulator [21], V_{min} of the μ C is held constant at 3.3 V even when V_{mote} is in the safe operating range of 3.3 V to 4.2 V. To gain insights on the performance of RW, we develop equations (1) and (2) by curve fitting the experimental results on V_{min}^{TI} and V_{min}^W for TI mote and the Waspote, respectively.

$$V_{min}^{TI} \approx 0.5965V_{mote} + 0.0316, \forall 1.8 \text{ V} \leq V_{mote} \leq 3.6 \text{ V} \quad (1)$$

$$V_{min}^W \approx \begin{cases} 0.4V_{mote} + 0.27, & 2.8 \text{ V} \leq V_{mote} < 3.2 \text{ V} \\ 1.6 \text{ V}, & 3.2 \text{ V} \leq V_{mote} \leq 4.2 \text{ V}. \end{cases} \quad (2)$$

B. Current Consumption Comparison of RW Scheme

Another set of experiments have been performed to find the current consumed by the interrupt pin (I_{int}). Current measurements for the TI mote using a Keithley 6485 picoammeter indicates that the current drawn is <10 nA at the instant of interrupt arrival. For the rest of time, current consumed is almost zero. We have noted that, as the interrupt pin that we have used on the Waspote is also used for alternative functions, it continuously draws a current in the range of 10 nA to 13 nA (shown in Fig. 10). We have also measured

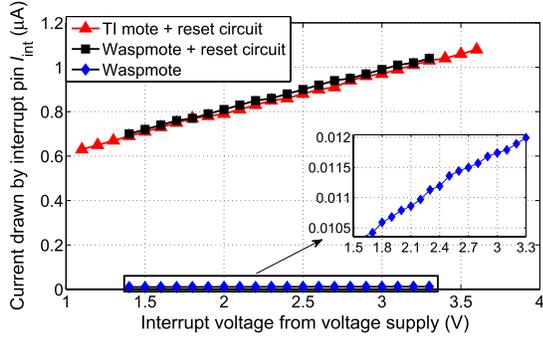


Fig. 10. Current consumed by interrupt pin.

the current consumed by the reset circuit [13] that is used in the REACH mote-based WuRx approach. Fig. 10 shows that, though the current consumed by the reset circuit is in the sub-micro ampere range, it is about 100 times more than that consumed directly by the interrupt pin. Thus, with our approach the interrupt pin does not load the RFEH.

C. Relation Between Wake-Up Range and Transmit Power

The relationship between received RF power and output DC voltage of RFEH [8] (Fig. 11), derived from experiments, is:

$$P_r (\text{mW}) = 0.0486 (V_{\text{out}}^{\text{DC}})^2 + 0.0659 V_{\text{out}}^{\text{DC}} - 0.0031. \quad (3)$$

Fig. 9 shows that the minimum required V_{mote} for operating TI mote and Wasp mote are respectively 1.8 V and 2.8 V. From Section V-A, $P_{r,\text{min}}$ of TI mote and Wasp mote at these V_{mote} values are determined by substituting $V_{\text{min}}^{\text{TI}} = 1.1$ V and $V_{\text{min}}^{\text{W}} = 1.4$ V for $V_{\text{out}}^{\text{DC}}$ in equation (3) as: $\{P_{r,\text{min}}^{\text{TI}}, P_{r,\text{min}}^{\text{W}}\} = \{-8.9213, -7.3420\}$ dBm. Further, by Friis transmission equation, the wake-up range is estimated as:

$$\text{Range (m)} = \lambda (P_t G_t G_r)^{\frac{1}{n}} (4\pi)^{-1} P_r^{-\frac{1}{n}} \quad (4)$$

where n is the path loss exponent, λ is the operating wavelength, P_t, P_r and G_t, G_r represent, respectively, the transmit power, receive power and gains of the transmitter and receiver antennae. Taking $G_t = 6.1$ dBi, $G_r = 6.1$ dBi, $n = 2$, and $\lambda = 0.328$ m, the maximum achievable wake-up range with our RFEH-WuRx prototype at various values of transmit power and V_{mote} is shown in Fig. 12. For a fixed RF transmit power, the wake-up range increases with reduction in the required V_{mote} . It shows that with the considered parameters, at transmit power of 1 W (or EIRP of 4 W), in the best case scenario ($V_{\text{mote}} = 1.8$ V $\Rightarrow V_{\text{min}} = 1.1$ V, from Fig. 9), the wake-up range is more than 9 m. Furthermore, with a 3 W EIRP Powercast transmitter [9], our achievable wake-up range of 8.06 m is marginally higher than 7.92 m range of REACH-mote [13].

VI. OPTIMIZED WURX DESIGN

Until now, we have studied how an RFEH can be reused as a WuRx to perform both RW and DW. However, the operable bit rate and wake-up range are bottlenecked by the characteristics of the RFEH. In this section, via ADS simulation studies

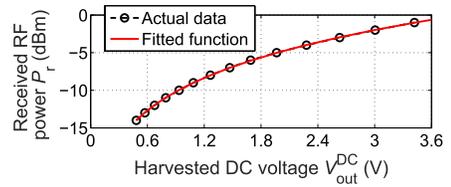
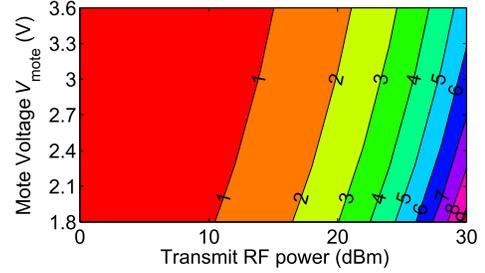
Fig. 11. Characteristic of our used RFEH. $C_{\text{stage}} = 36$ pF, $L = 68$ nH, $C = 3.3$ pF, $R = 220$ M Ω (cf. Fig. 13).

Fig. 12. Wake-up range (in m), indicated by numbers in the graph.

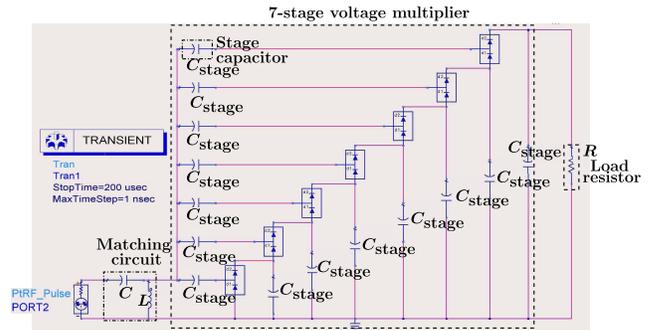


Fig. 13. ADS simulation block diagram of RFEH using a 7-stage Dickson voltage multiplier.

we design an optimized circuit solely for WuRx purposes that provides a much longer wake-up range for RW. We also show that an optimally-designed circuit solely for DW operation can support ID decoding at a higher bit rate and provide greater energy savings. These independent circuits trade-off cost to some extent for better WuRx characteristics, because a separate RFEH would be required for optimal energy harvesting. Specifically, in the optimized WuRx circuit design, we intend to improve the wake-up performance by analyzing the effect of all parameters, namely, C_{stage} , number of stages, R , and matching circuit (C, L) (cf. Fig. 13). Since $V_{\text{mote}} = 3.6$ V gives the wake-up range in the worst case scenario for a given transmit RF power (cf. Fig. 12), we consider $V_{\text{min}} = 2.2$ V that corresponds to $V_{\text{mote}} = 3.6$ V (cf. Fig. 9). By dividing $V_{\text{out}} = V_{\text{min}} = 2.2$ V with the current consumed by the interrupt pin of the μC ($= 10$ nA) (cf. Section V-B), the load R in the RFEH is found to be 220 M Ω . Unless otherwise stated, we consider $C_{\text{stage}} = 36$ pF, $R = 220$ M Ω , $C = 3.3$ pF, and $L = 68$ nH for generating simulation results.

A. Effect of Stage Capacitor C_{stage}

Since C_{stage} determines the sharpness of wake-up and hence bit rate in DW, optimum C_{stage} is decided in DW context.

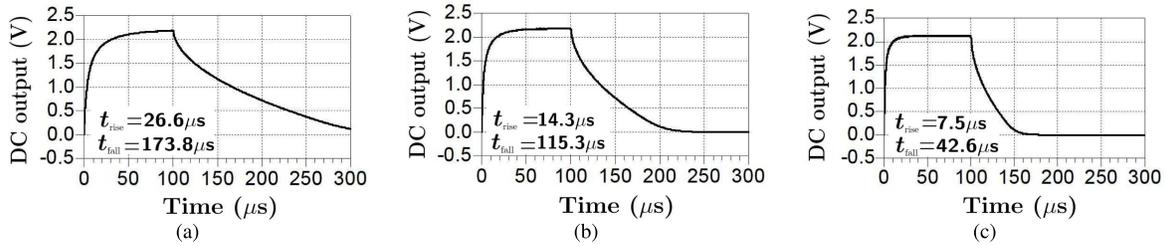


Fig. 14. Effect of C_{stage} value on t_{rise} and t_{fall} . $R = 220 \text{ M}\Omega$, $C = 3.3 \text{ pF}$, $L = 68 \text{ nH}$, stages = 7, $P_{\text{r,min}} = -3.4 \text{ dBm}$ for $V_{\text{out}}^{\text{DC}} = 2.2 \text{ V}$. (a) $C_{\text{stage}} = 36 \text{ pF}$ [8]. (b) $C_{\text{stage}} = 18 \text{ pF}$. (c) $C_{\text{stage}} = 9 \text{ pF}$.

Energy saving in DW is achieved if the μC remains in deep-sleep state (LPM3) for a longer period.¹ This can be achieved by reducing the time taken by the μC for ID decoding. The key idea is to reduce the rise time and fall time for a bit reception at the WuRx. As the stage capacitors store energy before delivering it to the load, the C_{stage} value highly affects the rise and fall times. In order to compare the optimized 7-stage design for WuRx with the 7-stage RFEH [8] used in our experiments (which was originally designed and optimized for charging a super-capacitor of a field node), we have conducted simulations.

We have performed transient simulation using Agilent ADS (block diagram shown in Fig. 13) to study the rise time (t_{rise}) and fall time (t_{fall}) of an RFEH by subjecting it to an RF signal pulse of duration $100 \mu\text{s}$. Results in Fig. 14 show the effect of C_{stage} on rise/fall times. The actual values of t_{rise} and t_{fall} for all C_{stage} values (36 pF, 18 pF, 9 pF) are also obtained. (t_{rise} is calculated as the time taken for the DC output to rise from 10% to 90% of the steady-state value. Similarly, t_{fall} is the time taken for the DC output to fall from 90% to 10% of the steady-state level). Reduction of C_{stage} beyond 9 pF introduces instability in steady-state response. Note that a single bit duration is mainly comprised of t_{rise} , t_{fall} and interrupt detection time (t_{detect}). As t_{detect} is low (sub- μs to a few μs [18]) compared to t_{rise} and t_{fall} , ($t_{\text{rise}} + t_{\text{fall}}$) contribute to the majority of bit duration. With $C_{\text{stage}} = 9 \text{ pF}$, $t_{\text{rise}} + t_{\text{fall}}$ reduces to one-fourth of the duration with $C_{\text{stage}} = 36 \text{ pF}$. This leads to about four times reduction in bit duration. Thus, a WuRx with $C_{\text{stage}} = 9 \text{ pF}$, when used for DW, *quadruples* the operable bit rate compared to that with $C_{\text{stage}} = 36 \text{ pF}$. As the energy consumption in DW is proportional to the decoding time, a reduction in bit duration results in energy savings. The same optimum C_{stage} value is used in the optimized WuRx for RW.

B. Effect of Matching Circuit (C , L) and Number of Stages

The load resistance R , matching circuit (C , L), and number of voltage multiplier stages in the RFEH in combination affect the wake-up range for a given transmitted RF power and the required $V_{\text{out}}^{\text{DC}}$ threshold. Therefore, optimization of these parameters are discussed in the context of RW.

Based on the observations from Fig. 14, we set $C_{\text{stage}} = 9 \text{ pF}$ and $R = 220 \text{ M}\Omega$ to find the optimal num-

¹For TI mote with $V_{\text{mote}} = 3 \text{ V}$, the measured power consumption in LPM3 and sleep state (LPM1) are $0.3 \mu\text{W}$ and $261.3 \mu\text{W}$, respectively.

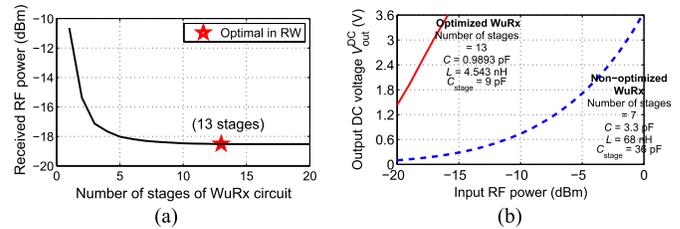


Fig. 15. Optimal WuRx for RW: (a) Effect of stages. $C_{\text{stage}} = 9 \text{ pF}$, $R = 220 \text{ M}\Omega$. (b) Comparison of optimized versus non-optimized WuRx circuit.

ber of stages. Harmonic balance analysis is used to find the optimum matching parameters ($C = 0.9893 \text{ pF}$ and $L = 4.543 \text{ nH}$), that provide the maximum DC output for RF input power in the range of -20 dBm to 0 dBm . $P_{\text{r,min}}$ required for $V_{\text{out}}^{\text{DC}} = 2.2 \text{ V}$ with varying number of stages is plotted in Fig.15(a). It shows that $P_{\text{r,min}}$ with increased number of stages gets saturated beyond 13-stages. So we conclude that 13-stage circuit design is optimal as it helps in achieving long wake-up range.

Fig. 15(b) plots the variation of $V_{\text{out}}^{\text{DC}}$ with RF input power for optimized WuRx design parameters ($C_{\text{stage}} = 9 \text{ pF}$, $L = 4.543 \text{ nH}$, $C = 0.9893 \text{ pF}$, and number of stages = 13) and non-optimized parameters ($C_{\text{stage}} = 36 \text{ pF}$, $C = 3.3 \text{ pF}$, $L = 68 \text{ nH}$, and number of stages = 7). We observe that the optimized design achieves significant improvement in wake-up performance over the RFEH-based WuRx design (cf. Section II). Fig. 15(b) shows that the optimized WuRx requires $P_{\text{r,min}}^{\text{TI}} = -18.52 \text{ dBm}$ ($= 0.01406 \text{ mW}$) for a minimum interrupt voltage $V_{\text{min}} = 2.2 \text{ V}$. In contrast, with non-optimized RFEH-based WuRx requires $P_{\text{r,min}}^{\text{TI}} = -3.41 \text{ dBm}$ ($= 0.4560 \text{ mW}$) for $V_{\text{min}} = 2.2 \text{ V}$. It may be noted that output DC voltage is clipped at 3.6 V with the help of a Zener diode to avoid damage to the μC . Thus, by optimizing the RFEH parameters there is a saving of about 96.92% ($= \frac{0.4560 - 0.01406}{0.4560} \times 100\%$) in the required RF power with optimized design, for achieving the same wake-up range as in the non-optimized design. For a Powercast transmitter with EIRP ($P_t G_t$) = 3 W , $\lambda = 0.328 \text{ m}$, and considering $G_r = 6.1 \text{ dBi}$, and $n = 2$, equation (4) gives the maximum achievable wake-up range by 13-stage independent WuRx design in the worst-case scenario ($V_{\text{min}} = 2.2 \text{ V}$) as 24.41 m , which is 5.69 times the wake-up range achieved by RFEH-based WuRx design ($= 4.29 \text{ m}$).

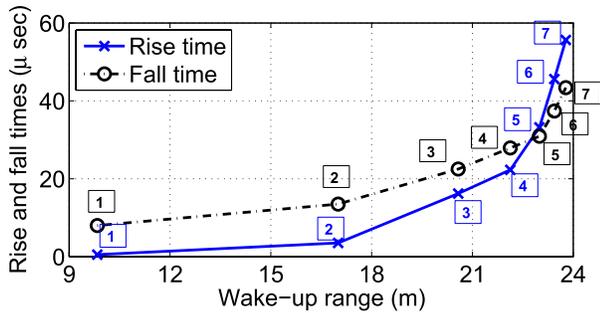


Fig. 16. Trade-off between rise/fall time and wake-up range in DW. Numbers in squared box indicate the number of stages (cf. Table II for optimum matching parameters (C , L) and $P_{r,\min}$).

TABLE II
EFFECT OF NUMBER OF STAGES WITH OPTIMIZED DW.
 $R = 220 \text{ M}\Omega$, $C_{\text{STAGE}} = 9 \text{ pF}$

No. of stages	C (pF)	L (nH)	t_{rise} (μs)	t_{fall} (μs)	$P_{r,\min}$ (dBm)
1	0.2832	46.310	0.5	8.0	-10.625
2	0.3647	26.540	3.5	13.5	-15.380
3	0.4608	18.260	16.2	22.5	-17.0357
4	0.5372	13.990	32.3	27.9	-17.667
5	0.5967	11.370	33.2	30.9	-18.000
6	0.6487	9.589	45.6	37.4	-18.1667
7	0.6965	8.287	55.6	43.4	-18.2917

C. Wake-Up Speed Versus Range Tradeoff in DW

While the load resistance ($R = 220 \text{ M}\Omega$) and its corresponding optimum matching circuit parameters remain valid for DW as well, an increased number of voltage multiplier stages reduces the wake-up speed ($\propto \frac{1}{t_{\text{rise}} + t_{\text{fall}}}$) while increasing the wake-up range. Therefore, in this section we study the optimum number of voltage multiplier stages as a tradeoff between the wake-up speed and the wake-up range.

As shown in Fig. 16, wake-up delay ($t_{\text{rise}} + t_{\text{fall}}$) monotonically increases with the number of stages. To save energy both at WuRx and WuTx, we further optimize the WuRx circuit to not only operate at a higher bit rate but also consume lower power. For this, we first perform harmonic balance simulations to find the optimum matching circuit parameters and then conduct transient simulations for each of the designs to find rise and fall times. Table II shows the optimum matching parameters (C , L), t_{rise} , t_{fall} for varying number of stages with $C_{\text{stage}} = 9 \text{ pF}$ and $R = 220 \text{ M}\Omega$. $P_{r,\min}$ denotes the minimum input power corresponding to a steady-state DC output value of 2.2 V, which is the same as $V_{\text{min}}^{\text{TI}}$ for $V_{\text{mote}} = 3.6 \text{ V}$. Although the capacitor values shown in Table II are low, they are realizable in practice [22].

Note that, due to non-optimized matching circuit parameters of the 7-stage RFEH used in the hardware experiments, $P_{r,\min} = -4.25 \text{ dBm}$ for generating an output voltage $V_{\text{out}} = 2.2 \text{ V}$ (cf. Fig. 11). On the other hand, optimized 7-stage WuRx design requires $P_{r,\min} = -18.2917 \text{ dBm}$ for $V_{\text{out}} = 2.2 \text{ V}$. Therefore, the optimized 7-stage design produces significantly large wake-up range compared to the non-optimized design. To make a fair comparison on the achievable bit rate, we consider the result

shown in Fig. 14(c) and the result from 7-stage design in Table II (because of having the same $C_{\text{stage}} = 9 \text{ pF}$ and number of stages = 7). Instead of comparing $t_{\text{rise}} + t_{\text{fall}}$ in both cases, we observe t_{rise} and t_{fall} separately. Since the input power is very low for the optimized 7-stage WuRx design ($= -18.2917 \text{ dBm}$), it takes more time ($55.6 \mu\text{s}$, from Table II) to rise to the steady state value of 2.2 V. On the other hand, with the non-optimized circuit, it is significantly lower ($7.5 \mu\text{s}$), which is due to the high input power. From this we conclude that t_{rise} is also affected by matching circuit parameters. t_{fall} is more or less constant in both cases (cf. Fig. 14(c) and Table II).

Fig. 16 plots the trade-off between the rise/fall times and the achievable wake-up range obtained using equation (4) with a Powercast transmitter EIRP ($P_t G_t$) = 3 W, $G_r = 6.1 \text{ dBi}$, $n = 2$, $\lambda = 0.328 \text{ m}$ and P_r as the minimum input power mentioned in Table II. Results show that the optimal number of stages in DW depends upon the specific requirement of having high wake-up speed or high wake-up range. It is observed that, due to the wake-up speed versus wake-up range tradeoff in DW, the optimal number stages is two because with a higher number of stages, the rise and fall times increase sharply without a significant improvement in wake-up range. Lower rise time and fall time with a 2-stage design as compared to a 7-stage design provides significant energy savings.

Energy saving per bit at the WuTx and WuRx can be estimated by multiplying the respective power consumption with the difference in $t_{\text{rise}} + t_{\text{fall}}$ in 7-stage design versus 2-stage design ($99.0 - 17.0 = 82 \mu\text{s}$, from Table II). For a 3 W EIRP transmitter, power consumption is 5 W [9]. So energy saving per bit in DW at the WuTx is 0.41 mJ. As mentioned in Section VI-A, power consumption at the μC is 0.261 mW ($3 \text{ V} \times 87.1 \mu\text{A}$) in LPM1 state. Hence, the energy saving per bit in DW at the WuRx is 21.40 nJ. Overall, the energy saving in the optimized 2-stage design over 7-stage design is 82.82%.

VII. CONCLUDING REMARKS

We have proposed a low-cost RFEH-based WuRx design capable of performing both RW and DW. Hardware implementation results have demonstrated that the proposed prototype can achieve a long wake-up range in RW and high accuracy in DW at a bit rate up to 33.33 kbps. Empirical results-assisted analytical modeling of wake-up range has shown that a node at a distance greater than 9 m from the WuTx can be woken up using either RW or DW. Further, using ADS simulations, we have studied the effect of RFEH parameters and proposed a 13-stage RFEH-based independent WuRx design with the optimized parameters for long range wake-up in RW. It has been estimated that using a 3 W EIRP Powercast transmitter, compared to a non-optimized design, an optimized design can increase the wake-up range by up to 5.69 times. We have also proposed an independent WuRx design for DW that is optimized for higher range as well as higher energy savings at both WuTx and WuRx (sensor node). Balancing the trade-off between rise/fall time and wake-up range, it has been shown that a 2-stage design is optimal in case of DW. This opti-

mized design can provide energy saving per bit of about 0.41 mJ and 21.40 nJ, respectively, at the WuTx and WuRx.

It may be noted that, an independent WuRx solution requires a separate antenna for its operation in addition to the RFEH circuit and an associated antenna for recharging the node, which incurs some additional cost. However, this approach allows the WuRx to flexibly operate on a different frequency. Further, independence of the WuRx operation from the RFEH suggests that this solution can be used in other WSNs.

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